STUDY AND EVALUATION SCHEME FOR DIPLOMA PROGRAMME IN ELECTRONICS & COMMUNICATION ENGINEERING

THIRD SEMESTER

Sr. No	Subject	L	T	P	TOT	EVALUATION SCHEME						Total Marks
						Internal Assessment		External Assessment (Examination)				
		Periods/Week				Theory	Practical	Theory		Practical		
						Max. Marks	Max. Marks	Max. Marks	Hrs	Max. Marks	Hrs	
3.1	Electrical Engineering and Machines	3	1	4	8	25	20	75	2.5	50	3.0	170
3.2	Analog Electronics	3	1	4	8	25	20	75	2.5	50	3.0	170
3.3	Digital Electronics	4	1	4	9	25	20	75	2.5	50	3.0	170
3.4	Communication Engineering	4	1	4	9	25	20	75	2.5	50	3.0	170
3.5	Electronics Workshop- cum-Minor Project	0	0	4	4	-	20	-	-	80	3.0	100
3.6	Computer Programming Using C	2	0	4	6	25	20	75	2.5	50	3.0	170
	General Proficiency #	-	-	4	4	-	25	-	-	-	-	25
	Industrial Exposure (Assessment at Inst. Level) +	-	-	-	-	-	25	-	-	-	-	25
	Total	16	4	28	48	125	170	375	-	330		1000

General Proficiency will comprise of various co-curricular activities like games, hobby clubs, seminars, declamation contests, extension lectures, NCC, NSS and cultural activities, elementary mathematics, GS & G.K etc.

+   Industrial visit compulsory at minimum 2 Industries or Department.

Note: 1- Each period will be 50 minutes.

  2- Each session will be of 16 weeks.

  3- Effective teaching will be at least 12.5 weeks.